

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	17133	((configuration adj word adj interface) or (control\$3 near3 delay)) and ("327"/\$.ccls. or "716"/\$.ccls.) (register near3 delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 20:02
S1	2	((delay adj circuit\$2) same tim\$3 same signal) and (monitor same phase) and control\$4 and (programmable adj delay) and (configur\$5 near3 word)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:28
S2	2	((delay adj circuit\$2) same tim\$3 same signal) and (monitor same phase) and control\$4 and (programmable adj delay) and (configur\$5 near3 word) and (circuit adj board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 10:48
S3	2	((delay adj circuit\$2) same tim\$3 same signal) and monitor and control\$4 and (programmable adj delay) and (configur\$5 near3 word) and (circuit adj board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:12
S4	5790	((delay adj circuit\$2) same tim\$3 same signal) and monitor and control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:12
S5	586	((delay adj circuit\$2) same tim\$3 same signal) and monitor and control\$4 and (circuit adj board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:12
S6	234	((delay adj circuit\$2) same tim\$3 same signal) and monitor and control\$4 and (circuit adj board) and receiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:13
S7	54	((delay adj circuit\$2) same tim\$3 same signal) and (monitor same phase) and control\$4 and (circuit adj board) and receiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:14
S8	0	((delay adj circuit\$2) same tim\$3 same signal) and (monitor same phase) and control\$4 and (circuit adj board) and receiver and ("716"/\$.ccls. or "257"/\$.ccls. or "438"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:15

S9	166757	((delay or tim\$3) near3 (configur\$4 or program\$5) same (unit or device or circuit\$3)) and pcb and receiver and controller amd monitor and phase	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:54
S10	12969	((delay or tim\$3) near3 (configur\$4 or program\$5) same (unit or device or circuit\$3)) and pcb and receiver and controller amd monitor and phase and (configuration near3 word)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:37
S11	2	((programmable adj delay) near3 (unit or device or circuit\$3)) and (configuration adj word adj setting)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:40
S12	1322	((programmable adj delay) near3 (unit or device or circuit\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:39
S13	1	((programmable adj delay) near3 (unit or device or circuit\$3)) and (configuration adj word adj interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:40
S14	2	((programmable adj delay) near3 (unit or device or circuit\$3)) and (configuration adj word)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:43
S15	710	((programmable adj delay) near3 (unit or device or circuit\$3)) same control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:44
S16	82	((programmable adj delay) near3 (unit or device or circuit\$3)) same control\$4 and receiver and monitor and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:45
S17	3	((programmable adj delay) near3 (unit or device or circuit\$3)) same control\$4 and receiver and monitor and channel and pcb	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 08:46
S18	82	((programmable adj delay) near3 (unit or device or circuit\$3)) same control\$4 and receiver and monitor and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 17:49

S19	1	((programmable adj delay) near3 (unit or device or circuit\$3)) same (control\$4 or coordinat\$3 or program\$4) same (timing near3 signal) and receiver and monitor and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 09:35
S20	46	((programmable adj delay) near3 (unit or device or circuit\$3)) and (control\$4 or coordinat\$3 or program\$4) and (timing near3 signal) and receiver and monitor and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 10:37
S21	30	((programmable adj delay) near3 (unit or device or circuit\$3)) and (control\$4 or coordinat\$3 or program\$4) and (timing near3 signal) and receiver and monitor and channel and (chip or pcb)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 09:37
S22	23	((programmable adj delay) near3 (unit or device or circuit\$3)) and (control\$4 or coordinat\$3 or program\$4) and (timing near3 signal) and receiver and monitor and channel and interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 10:38
S23	6795	receiver same (delay near3 (unit or line))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:50
S24	3	receiver same (delay near3 (unit or line)); same channel same interface same (control\$3 or coordinat\$3) and (pcb or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:35
S25	6	(timing near3 adjustment) same ((delay or timing) near3 (program\$5 or configur\$5)) and ((configuration adj word) or (communication adj port))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:39
S26	19	(programmable adj delay) same (unit or line or device) and channel and (communication adj port) and (pcb or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:42
S27	78878	(communication near3 port) or (configuration adj word adj interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:43

S28	18	((communication near3 port) or (configuration adj word adj interface)) same receiver and (programmable near3 delay) and channel and (pcb or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:47
S29	1	((communication near3 port) or (configuration adj word adj interface)) same (receiver near3 channel) and (programmable near3 delay) and (pcb or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:48
S30	1	(configuration adj word adj interface) and (programmable adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:48
S31	102	(communication near3 port) and (programmable adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:49
S32	0	(communication near3 port) and (programmable adj delay) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:49
S33	8	receiver same (delay near3 (unit or line)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:53
S34	343	(receiver near3 channel) same (delay near3 (unit or line))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:53
S35	24	(receiver near3 channel) same (delay near3 (unit or line)) same (programmable or configur\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:53
S36	30	(receiver near3 channel) same (delay near3 (unit or line)) same (program\$5 or configur\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:58
S37	0	receiver same (programmable adj delay) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 11:59

S38	2	receiver same ((program\$5 or configur\$5) adj delay) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 12:05
S39	30	receiver same ((program\$5 or configur\$5) adj delay) same chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:10
S40	1	receiver near3 ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:14
S41	1	receiver same ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:15
S42	123	receiver same ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port) or phase or communication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:16
S43	77	receiver same ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port) or phase or communication) and (chip or pcd)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:41
S44	1	receiver same ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port) or phase or communication) and (chip or pcd) and (slow adj serial adj communication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:41

S45	1	receiver same ((program\$5 or configur\$5) adj delay) same ((configuration adj word adj interface) or (communication near3 port) or (phase adj voltage adj converter) or (serial adj port) or phase or communication) and (chip or pcd) and (slow adj3 data)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:44
S48	0	receiver adj "with" adj programmable adj delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:47
S49	59	receiver near3 programmable near3 delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:50
S50	32341	receiver near3 channels	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 16:50
S51	19312	receiver adj3 channels	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:54
S52	1	((receiver or circuit or element or circuitry) adj3 channels) same (configuration adj word adj interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:37
S53	156	((receiver or circuit or element or circuitry) adj3 channels) same ((configuration adj word adj interface) or ((control\$3 or coordinat\$3) near3 (timing or delay) near3 channels))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:39
S54	14530	((delay or tim\$3) near3 (configur\$4 or program\$5) same (unit or device or circuit\$3)) and pcb and receiver and controller amd monitor and phase and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:54
S55	233	receiver adj3 channels and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:55

S56	11	(receiver adj3 channels) same interface and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:57
S57	1	(configuration adj word adj interface) and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 07:58
S58	4459	((configuration adj word adj interface) or (control\$3 near3 delay)) and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 19:27
S59	5	((configuration adj word adj interface) or (control\$3 near3 delay)) same receiver same (programmable adj delay) and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 08:07
S60	5	((configuration adj word adj interface) or (control\$3 near3 delay) or (phase adj voltage adj converter) or (serial adj port adj3 circuit)) same receiver same (programmable adj delay) and "327"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:23
S61	2	(configuration adj word adj interface) and ("327"/\$.ccls. or "716"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:26
S62	1	chip same ((timing or delay) near3 (adjustment or control\$3 or coordinat\$3)) same (channel near3 (plurality or multi\$2)) same (fast adj data)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:38
S63	7156	uart	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:38
S64	79	uart and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:44
S65	0	uart same channels same (timing or delay) and ("716"/\$.ccls. or "327"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:42

S66	58	uart and (timing or delay) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:44
S67	16	uart and (timing or delay) and channels and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:44